

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	16	test\$3 near5 mixed signal semiconductor device\$1	US_PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:07
L2	40366	device interface	US_PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:07
L3	17967	device under test	US_PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:08
L7	25	control unit and (processor and operating system and computational modules)	US_PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:11
L8	118	device testing unit	US_PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:11
L10	12	1 and (2 or 3 or 7 or 8)	US_PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:11

L15	1	executing second test same (((concurrently) or (parallel) or (simultaneously)) or (at the same time)) and (processing adj (test data) or (test result near2 first test))	US-PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:21
L16	5543	((concurrently) or (parallel) or (simultaneously) or (at the same time)) and (first test same second test)	US-PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:24
L17	1	l0 and l6	US-PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:24
L18	15322	714/724,725,733,734,10,11,30,38,742,740,776;702/118;716/4.xls.	US-PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:25
L19	400	l8 and (l0 or l6)	US-PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:25
L20	5	l8 and l0	US-PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:26
L22	1	l0 and l6	US-PGPUB; USPAT; USCCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/04/26 09:26

4/26/09 9:47:21 AM

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